

CLAIM AMENDMENT

Please **AMEND** claims 1 and 9, as follows.

1. (Currently Amended) A liquid crystal display, comprising:
 - a first substrate;
 - a plurality of gate lines formed on the first substrate and transmitting scanning signals;
 - a plurality data lines crossing over the gate lines and transmitting picture signals;
 - a plurality of pixels defined by the gate lines and the data lines, the gate lines dividing the pixel electrode into rows and the data lines dividing the pixel electrodes into columns;
 - a protective layer formed over the gate lines and data lines;
 - a plurality of pixel of electrodes formed on the protective layer, each pixel electrode corresponding to one of the plurality of pixels;
 - a second substrate facing the first substrate;
 - a liquid crystal layer formed between said first substrate and said second substrate;
 - ~~a plurality of pixels demarcated by the gate lines and the data lines, the gate lines demarcating the pixels into rows, and the data lines demarcating the pixels into columns;~~
 - a black matrix defining each pixel; and
 - wherein: a pixel electrode formed in each pixel; and
 - storage capacitance formed is between each pixel electrode and the gate line of a previous row, and
 - ~~wherein~~ an opening ratio of the pixels on a first row is different from that of the pixels on the other rows.

2. (Previously Amended) The liquid crystal display of claim 1, wherein the opening ratio of the pixels on the first row is smaller than that of the pixels on the other rows.

3. (Original) The liquid crystal display of claim 2, wherein the difference in the opening ratio is made by differentiating an opening area of the black matrix.

4. (Previously Amended) The liquid crystal display of claim 3, wherein the black matrix is formed on the second substrate.

5-7. (Withdrawn)

8. (Previously Amended) The liquid crystal display of claim 2, wherein the opening ratio of the pixels on the first row is 60% to 80% of that of the pixels on the other rows.

9. (Currently Amended) A liquid crystal display, comprising:
a first substrate;
a plurality of gate lines formed on the first substrate and transmitting scanning signals;
a plurality of data lines crossing over the gate lines and transmitting picture signals;
a plurality of pixels defined by the gate lines and the data lines, the gate lines dividing the pixels into rows and the data lines dividing the pixels into columns;
a protective layer formed over the gate lines and the data lines;
a plurality of pixel electrodes formed on the protective layer, each pixel electrode corresponding to one of the plurality of pixels;

a second substrate facing the first substrate;
a liquid crystal layer formed between said first substrate and said second substrate;
~~a plurality of pixels demarcated by the gate lines and the data lines, the gate lines~~
~~demarcating the pixels into rows, and the data lines demarcating the pixels into columns;~~
a black matrix defining each pixel; and
~~a pixel electrode formed in each pixel;~~
a storage capacitor line formed on said first substrate parallel to the gate line, the storage capacitor line overlapping the pixel electrodes on the first row;₁

wherein:

first storage capacitance is formed between each pixel electrode and the gate line of a previous row;₂ and;

second storage capacitance is formed between each pixel electrode on the first row and said storage capacitor line,

~~wherein~~ a gate-off voltage or a common electrode voltage is applied to said storage capacitor line, and

an opening ratio of each pixel on the first row is different from that of all the pixels on the other rows.

10. (Previously Cancelled)

11. (Previously Amended) The liquid crystal display of claim 9, wherein the opening ratio of the pixels on the first row is smaller than that of the pixels on the other rows.

12. (Original) The liquid crystal display of claim 11, wherein the difference in the opening ratio is made by differentiating an opening area of said black matrix.
13. (Previously Amended) The liquid crystal display of claim 12, wherein said black matrix is formed on said second substrate.
14. (Previously Amended) The liquid crystal display of claim 13, wherein an opening width of said black matrix on the first row is identical to that of said black matrix on the other rows.
15. (Previously Amended) The liquid crystal display of claim 13, wherein an opening length of said black matrix on the first row is smaller than that of said black matrix on the other rows.
- 16-18. (Withdrawn)
19. (Previously Amended) The liquid crystal display of claim 11, wherein the opening ratio of the pixels on the first row is 60% to 80% of that of the pixels on the other rows.
20. (Original) The liquid crystal display of claim 9, further comprising a gate-off line formed on said first substrate to transmit a gate-off voltage.

21. (Original) The liquid crystal display of claim 20, wherein the gate-off line and said storage capacitor line are formed at the same layer as the gate line.

22. (Original) The liquid crystal display of claim 21, wherein the gate-off line and said storage capacitor line are electrically connected to each other via a connection member, and the connection member is formed at the same layer as the data line or said pixel electrode.

23. (Previously Amended) The liquid crystal display of claim 9, further comprising:
gate signal transmission films arranged on said first substrate and provided with a gate driving integrated circuit electrically connected to the gate lines for applying gate driving signals thereto; and

data signal transmission films arranged on said first substrate and provided with a data driving integrated circuit electrically connected to the data lines for applying data driving signals thereto,

wherein a common electrode wire for applying the common electrode voltage, a gate-on wire for applying a gate on-voltage to the TFTs controlling the picture signals, a gate-off wire for applying the gate off-voltage, and wires for transmitting a carry-in signal or a gate-clock signal are extended from an edge portion of the first substrate between the gate signal transmission film and the data signal transmission film.

24. (Original) The liquid crystal display of claim 23, the common electrode wire, the gate-on wire and the gate-off wire are formed at the same layer as the gate lines with the same material.

25-51. (Withdrawn)